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 Smart Computing

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Soft Computing Apple Academic Press
DIGITAL SYSTEMS DESIGN USING VERILOG integrates coverage of logic design principles, Verilog as a hardware design language, and FPGA implementation to help electrical and computer engineering students master the process of designing and testing new hardware configurations. A Verilog equivalent of authors Roth and John's previous successful text using VHDL, this practical book presents Verilog constructs side-by-side with hardware, encouraging students to think in terms of desired hardware while writing synthesizable Verilog. Following a review of the basic concepts of logic design, the authors introduce the basics of Verilog using simple combinational circuit examples, followed by models for simple sequential circuits. Subsequent chapters ask readers to tackle more and more complex designs. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

The Role of Internet of Things (IoT) in Biomedical Engineering Springer

With all the material available in the field of artificial intelligence (AI) and soft computing-texts, monographs, and journal articles-there remains a serious gap in the literature. Until now, there has been no comprehensive resource accessible to a broad audience yet containing a depth and breadth of information that enables the reader to fully understand and readily apply AI and soft computing concepts. Artificial Intelligence and Soft Computing fills this gap. It presents both the traditional and the modern aspects of AI and soft computing in a clear, insightful, and highly comprehensive style. It provides an in-depth analysis of mathematical models and algorithms and demonstrates their applications in real world problems. Beginning with the behavioral perspective of "human cognition," the text covers the tools and techniques required for its intelligent realization on machines. The author addresses the classical aspects-search, symbolic logic, planning, and machine learning-in detail and includes the latest research in these areas. He introduces the modern aspects of soft computing from first principles and discusses them in a manner that enables a beginner to grasp the

subject. He also covers a number of other leading aspects of AI research, including nonmonotonic and spatio-temporal reasoning, knowledge acquisition, and much more. *Artificial Intelligence and Soft Computing: Behavioral and Cognitive Modeling of the Human Brain* is unique for its diverse content, clear presentation, and overall completeness. It provides a practical, detailed introduction that will prove valuable to computer science practitioners and students as well as to researchers migrating to the subject from other disciplines.

Proceedings of the Eleventh National Conference on Communications Springer Science & Business Media

This book gathers high-quality papers presented at the International Conference on Smart Trends for Information Technology and Computer Communications (SmartCom 2020), organized by the Global Knowledge Research Foundation (GR Foundation) from 23 to 24 January 2020. It covers the state-of-the-art and emerging topics in information, computer communications, and effective strategies for their use in engineering and managerial applications. It also explores and discusses the latest technological advances in, and future directions for, information and knowledge computing and its applications.

VLSI Physical Design: From Graph Partitioning to Timing Closure Springer

How should I prepare for a Digital VLSI Verification Interview? What all topics do I need to know before I turn up for an interview? What all concepts do I need to brush up? What all resources do I have at my disposal for preparation? What does an Interviewer expect in an Interview? These are few questions almost all individuals ponder upon before an interview. If you have these questions in your mind, your search ends here as keeping these questions in their minds, authors have written this book that will act as a golden reference for candidates preparing for Digital VLSI Verification Interviews. Aim of this book is to enable the readers practice and grasp important concepts that are applicable to Digital VLSI Verification domain (and Interviews) through Question and Answer approach. To achieve this aim, authors have not restricted themselves just to the answer. While answering the questions in this book, authors have taken utmost care to explain underlying fundamentals and concepts. This book consists of 500+ questions covering wide range of topics that test fundamental concepts through problem statements (a common interview practice which the authors have seen over last several years). These questions and problem statements are spread across nine chapters and each chapter consists of questions to help readers brush-up, test, and hone fundamental concepts that form basis of Digital VLSI Verification. The scope of this book however, goes beyond technical concepts. Behavioral skills also form a critical part of working culture of any company. Hence, this book consists of a section that lists down behavioral interview questions as well. Topics covered in this book: 1. Digital Logic Design (Number Systems, Gates, Combinational, Sequential Circuits, State Machines, and other Design problems) 2. Computer Architecture (Processor Architecture, Caches, Memory Systems) 3. Programming (Basics, OOP, UNIX/Linux, C/C++, Perl) 4. Hardware Description Languages (Verilog, SystemVerilog) 5. Fundamentals of Verification (Verification Basics, Strategies, and Thinking problems) 6. Verification Methodologies (UVM, Formal, Power, Clocking, Coverage, Assertions) 7. Version Control Systems (CVS, GIT, SVN) 8. Logical Reasoning/Puzzles (Related to Digital Logic, General Reasoning, Lateral Thinking) 9. Non Technical and Behavioral Questions (Most commonly asked) In addition to technical and behavioral part, this book touches upon a typical interview process and gives a glimpse of latest interview trends. It also lists some general tips and Best-Known-Methods to enable

the readers follow correct preparation approach from day-1 of their preparations. Knowing what an Interviewer looks for in an interviewee is always an icing on the cake as it helps a person prepare accordingly. Hence, authors of this book spoke to few leaders in the semiconductor industry and asked their personal views on "What do they look for while Interviewing candidates and how do they usually arrive at a decision if a candidate should be hired?". These leaders have been working in the industry from many-many years now and they have interviewed lots of candidates over past several years. Hear directly from these leaders as to what they look for in candidates before hiring them. Enjoy reading this book. Authors are open to your feedback. Please do provide your valuable comments, ratings, and reviews. *Indian National Bibliography* Springer Science & Business Media

This book gives insight into the emerging semiconductor devices from their applications in electronic circuits. It discusses the challenges in the field of engineering and applications of advanced low-power devices. *Emerging Low-Power Semiconductor Devices: Applications for Future Technology Nodes* offers essential exposure to low-power devices, and applications in wireless, biosensing, and circuit domains. This book provides a detailed discussion on all aspects, including the current and future scenarios related to the low-power device. The book also presents basic knowledge about field-effect transistor (FET) devices and introduces emerging and novel FET devices. The chapters include a review of the usage of FET devices in various domains like biosensing, wireless, and cryogenics applications. The chapters also explore device-circuit co-design issues in the digital and analog domains. The content is presented in an easy-to-follow manner that makes it ideal for individuals new to the subject. This book is intended for scientists, researchers, and postgraduate students looking for an understanding of device physics, circuits, and systems.

Energy Research Abstracts Corwin Press

This book provides a comprehensive overview of characterization techniques and advanced modeling of VLSI circuits for modern and advanced process nodes for timing, power, noise and variation models. Intended audience includes research professionals, graduate students, circuit and PDK designers, characterization engineers, CAD developers, managers, mentors, and the merely curious. It is organized to serve as a compendium to a beginner, a ready reference to intermediate and source for an expert.

VLSI Design and Test for Systems Dependability Springer

This comprehensive text on switching theory and logic design is designed for the undergraduate students of electronics and communication engineering, electrical and electronics engineering, electronics and instrumentation engineering, telecommunication engineering, computer science and engineering, and information technology. It will also be useful to AMIE, IETE and diploma students. Written in a student-friendly style, this book, now in its Second Edition, provides an in-depth knowledge of switching theory and the design techniques of digital circuits. Striking a balance between theory and practice, it covers topics ranging from number systems, binary codes, logic gates and Boolean algebra to minimization using K-maps and tabular method, design of combinational logic circuits, synchronous and asynchronous sequential circuits, and algorithmic state machines. The book discusses threshold gates and programmable logic devices (PLDs). In addition, it elaborates on flip-flops and shift registers. Each chapter includes several fully worked-out examples so that the students get a thorough grounding in related design concepts. Short questions with answers, review questions, fill in the blanks, multiple choice questions and problems are provided at the end of each chapter.

These help the students test their level of understanding of the subject and prepare for examinations confidently. NEW TO THIS EDITION • VHDL programs at the end of each chapter • Complete answers with figures • Several new problems with answers
Characterization and Modeling of Digital Circuits Springer Nature
 This book constitutes the refereed proceedings of the 17th International Symposium on VLSI Design and Test, VDAT 2013, held in Jaipur, India, in July 2013. The 44 papers presented were carefully reviewed and selected from 162 submissions. The papers discuss the frontiers of design and test of VLSI components, circuits and systems. They are organized in topical sections on VLSI design, testing and verification, embedded systems, emerging technology.

Security and Fault Tolerance in Internet of Things Springer

This book comprises select proceedings of the International Conference on VLSI, Communication and Signal processing (VCAS 2018). It looks at latest research findings in VLSI design and applications. The book covers a wide range of topics in electronics and communication engineering, especially in the area of microelectronics and VLSI design, communication systems and networks, and image and signal processing. The contents of this book will be useful to researchers and professionals alike.

Digital Systems Design Using Verilog Springer Nature

Design and optimization of integrated circuits are essential to the creation of new semiconductor chips, and physical optimizations are becoming more prominent as a result of semiconductor scaling. Modern chip design has become so complex that it is largely performed by specialized software, which is frequently updated to address advances in semiconductor technologies and increased problem complexities. A user of such software needs a high-level understanding of the underlying mathematical models and algorithms. On the other hand, a developer of such software must have a keen understanding of computer science aspects, including algorithmic performance bottlenecks and how various algorithms operate and interact. "VLSI Physical Design: From Graph Partitioning to Timing Closure" introduces and compares algorithms that are used during the physical design phase of integrated-circuit design, wherein a geometric chip layout is produced starting from an abstract circuit design. The emphasis is on essential and fundamental techniques, ranging from hypergraph partitioning and circuit placement to timing closure.

Trustworthy Hardware Design: Combinational Logic

Locking Techniques Springer Nature

This volume introduces the key evolving applications of IoT in the medical field for patient care delivery through the usage of smart devices. It shows how IoT opens the door to a wealth of relevant healthcare information through real-time data analysis as well as testing, providing reliable and pragmatic data that yields enhanced solutions and discovery of previously undiscovered issues. The Role of Internet of Things (IoT) in Biomedical Engineering: Present Scenario and Challenges discusses IoT devices that are deployed for enabling patient health tracking, various emergency issues, smart administration of patients, etc. It looks at the problems of cardiac analysis in e-healthcare, explores the employment of smart devices aimed for different patient issues, and examines the usage of Arduino kits where the data can be transferred to cloud for internet-based uses. The volume also considers the roles of IoT in electroencephalography (EEG) and magnetic resonance imaging (MRI), which play significant roles in biomedical applications. This book also incorporates the use of IoT applications for smart wheelchairs, telemedicine, GPS positioning of heart patients, smart administration with drug tracking, and more. Key features:
 Explores the use of IoT in the field of biomedical engineering

Discusses current issues associated with biomedical engineering while including the fundamentals such as collaboration on usage of sensors, bio-interfaces, e-medicine, remote healthcare, etc. Throws light on IoT for healthcare monitoring as well as for remote healthcare, data communication, monitoring, and diagnosis. The book will help readers to keep abreast of the current novel technologies for conducting research while employing various diagnostic tools and to explore frontiers of what is realizable in practice

VLSI Design and Test PHI Learning Pvt. Ltd.

This book constitutes the refereed proceedings of the Second International Conference on Smart Trends in Information Technology and Computer Communications, SmartCom 2017, held in Pune, India, in August 2017. The 38 revised papers presented were carefully reviewed and selected from 310 submissions. The papers address issues on smart and secure systems; smart and service computing; smart data and IT innovations.

Advanced Computing CRC Press

The book is a collection of peer-reviewed best selected research papers presented at the International Conference on Data Intelligence and Cognitive Informatics (ICDICI 2021), organized by SCAD College of Engineering and Technology, Tirunelveli, India, during July 16–17, 2021. This book discusses new cognitive informatics tools, algorithms, and methods that mimic the mechanisms of the human brain which leads to an impending revolution in understating a large amount of data generated by various smart applications. The book includes novel work in data intelligence domain which combines with the increasing efforts of artificial intelligence, machine learning, deep learning, and cognitive science to study and develop a deeper understanding of the information processing systems.

Cracking Digital VLSI Verification Interview World Scientific

This book comprises the proceedings of the International Conference on VLSI & Microwave and Wireless Technologies (ICVMWT-2021). The book includes peer-reviewed papers on the core technological developments in emerging fields like wireless communication, RF microwave/radar, VLSI, optical communication, etc. The book will serve as a valuable reference resource for academics and researchers across the globe.

Index to American Doctoral Dissertations Springer Nature

The field of SMART technologies is an interdependent discipline. It involves the latest burning issues ranging from machine learning, cloud computing, optimisations, modelling techniques, Internet of Things, data analytics, and Smart Grids among others, that are all new fields. It is an applied and multi-disciplinary subject with a focus on Specific, Measurable, Achievable, Realistic & Timely system operations combined with Machine intelligence & Real-Time computing. It is not possible for any one person to comprehensively cover all aspects relevant to SMART Computing in a limited-extent work. Therefore, these conference proceedings address various issues through the deliberations by distinguished Professors and researchers. The SMARTCOM 2020 proceedings contain tracks dedicated to different areas of smart technologies such as Smart System and Future Internet, Machine Intelligence and Data Science, Real-Time and VLSI Systems, Communication and Automation Systems. The proceedings can be used as an advanced reference for research and for courses in smart technologies taught at graduate level.

Proceedings of the International Conference on Paradigms of Computing, Communication and Data Sciences Allied Publishers

This book presents select peer-reviewed proceedings of the 2nd International Conference on Advances in VLSI and Embedded Systems (AVES 2021). This book covers cutting-edge original

research in VLSI design, devices and emerging technologies, embedded systems, and CAD for VLSI. To address the demand for complex and high-functionality systems as well as portable consumer electronics, the contents focus on advanced topics of circuit and systems design, fabrication, testing, and standardization. This book is useful for students, researchers as well as industry professionals interested in emerging trends in VLSI and embedded systems.

Smart Trends in Information Technology and Computer Communications Allied Publishers

This book covers active R filters, OTA-C filters, and switched-capacitor filters, including topics such as differential output opamps, sensitivity analysis for passive components, multiple-feedback techniques, double-sampling, and N-path filters.

CMOS analog circuit design CRC Press

This book covers various aspects of security, privacy and reliability in Internet of Things (IoT) and Cyber-Physical System design, analysis and testing. In particular, various established theories and practices both from academia and industry are presented and suitably organized targeting students, engineers and researchers. Fifteen leading academicians and practitioners wrote this book, pointing to the open problems and biggest challenges on which research in the near future will be focused.

Compact Modeling Independently Published

Practical Low Power Digital VLSI Design emphasizes the optimization and trade-off techniques that involve power dissipation, in the hope that the readers are better prepared the next time they are presented with a low power design problem. The book highlights the basic principles, methodologies and techniques that are common to most CMOS digital designs. The advantages and disadvantages of a particular low power technique are discussed. Besides the classical area-performance trade-off, the impact to design cycle time, complexity, risk, testability and reusability are discussed. The wide impacts to all aspects of design are what make low power problems challenging and interesting. Heavy emphasis is given to top-down structured design style, with occasional coverage in the semicustom design methodology. The examples and design techniques cited have been known to be applied to production scale designs or laboratory settings. The goal of Practical Low Power Digital VLSI Design is to permit the readers to practice the low power

techniques using current generation design style and process technology. Practical Low Power Digital VLSI Design considers a wide range of design abstraction levels spanning circuit, logic, architecture and system. Substantial basic knowledge is provided for qualitative and quantitative analysis at the different design abstraction levels. Low power techniques are presented at the circuit, logic, architecture and system levels. Special techniques that are specific to some key areas of digital chip design are discussed as well as some of the low power techniques that are just appearing on the horizon. Practical Low Power Digital VLSI Design will be of benefit to VLSI design engineers and students who have a fundamental knowledge of CMOS digital design.

Modern Applications of Automata Theory Springer Nature

The leading experts in system change and learning, with their school-based partners around the world, have created this essential companion to their runaway best-seller, Deep Learning: Engage the World Change the World. This hands-on guide provides a roadmap for building capacity in teachers, schools, districts, and systems to design deep learning, measure progress, and assess conditions needed to activate and sustain innovation. Dive Into Deep Learning: Tools for Engagement is rich with resources educators need to construct and drive meaningful deep learning experiences in order to develop the kind of mindset and know-how that is crucial to becoming a problem-solving change agent in our global society. Designed in full color, this easy-to-use guide is loaded with tools, tips, protocols, and real-world examples. It includes:

- A framework for deep learning that provides a pathway to develop the six global competencies needed to flourish in a complex world — character, citizenship, collaboration, communication, creativity, and critical thinking.
- Learning progressions to help educators analyze student work and measure progress.
- Learning design rubrics, templates and examples for incorporating the four elements of learning design: learning partnerships, pedagogical practices, learning environments, and leveraging digital.
- Conditions rubrics, teacher self-assessment tools, and planning guides to help educators build, mobilize, and sustain deep learning in schools and districts. Learn about, improve, and expand your world of learning. Put the joy back into learning for students and adults alike. Dive into deep learning to create learning experiences that give purpose, unleash student potential, and transform not only learning, but life itself.